

General-Purpose Low-Voltage Open-Drain Output Comparator

Product Description

The LMV331 and LMV393 is the single and dual comparator version, the LMV339 is quad comparator version, and both are open-drain output comparators for maximum flexibility. It can operate from 1.8V to 5.5V, and have low power consuming 50 μ A (TYP) per channel.

The LMV331, LMV393 and LMV339 are the most cost- effective solutions for applications where low voltage operation, low power and space saving are the primary specifications in circuit design for portable consumer products.

The LMV331, LMV393 and LMV339 are available in Green SOT23-5, SC70-5, SOP8, MSOP8, SOP14 and TSSOP14 packages. operates over an ambient temperature range of -40°C to +125°C.

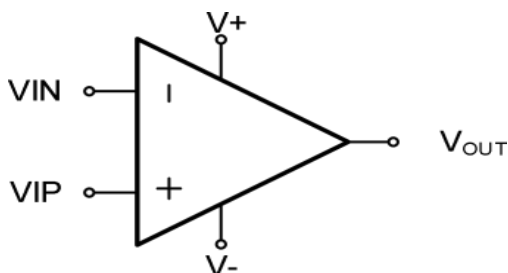
Features

- Supply range: +1.8V to +5.5V
- Low supply current 50 μ A per channel at $V_s = 5V$
- Input common-Mode voltage range includes ground
- Operating temperature from -40°C to +125°C
- Low output saturation voltage 100mV typical
- Open-Drain output for maximum flexibility

Applications

- Hysteresis comparators
- Oscillators
- Window comparators
- Industrial equipment
- Test and measurement

Circuit Diagram



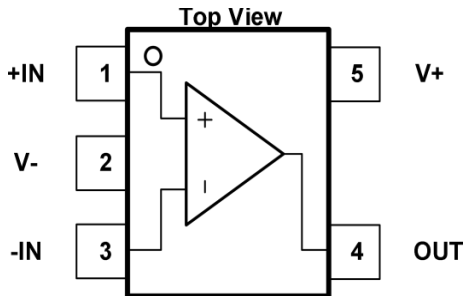
Device Summary, Pin and Packages

Table. 5-1. Pin Definition

Serial Name	Part Name	Package	Body Size (Nom)	Marking ⁽²⁾	MSL ⁽³⁾	Package Qty
LMV331	LMV331W	SOT23-5	2.90mm×1.60mm×1.10mm	LMV331 WDJXX	3	Tape and Reel,3000
LMV331	LMV331C	SC70-5	2.10mm×1.25mm×1.00mm	LMV331 WDJXX	3	Tape and Reel,3000
LMV393	LMV393IDRW	SOP8	6.00mm×3.90mm×1.75mm	LMV393 WDJXX	3	Tape and Reel,2500
LMV393	LMV393M8	MSOP8	3.00mm×3.00mm×1.10mm	LMV393 WDJXX	3	Tape and Reel,2500
LMV339	LMV339IDRW	SOP14	8.65mm×3.90mm×1.75mm	LMV339 WDJXX	3	Tape and Reel,2500
LMV339	LMV339MTW	TSSOP14	5.00mm×4.40mm×1.20mm	LMV339 WDJXX	3	Tape and Reel,2500

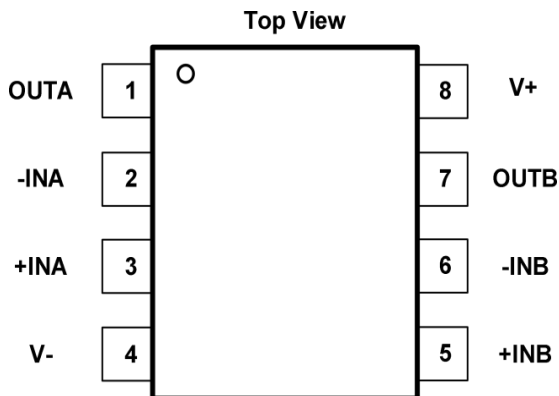
- (1) For all available packages, please contact product sales.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.
- (4) WDJ is logo, "XX" in Marking will be appeared as the batch code.

Pin Configuration



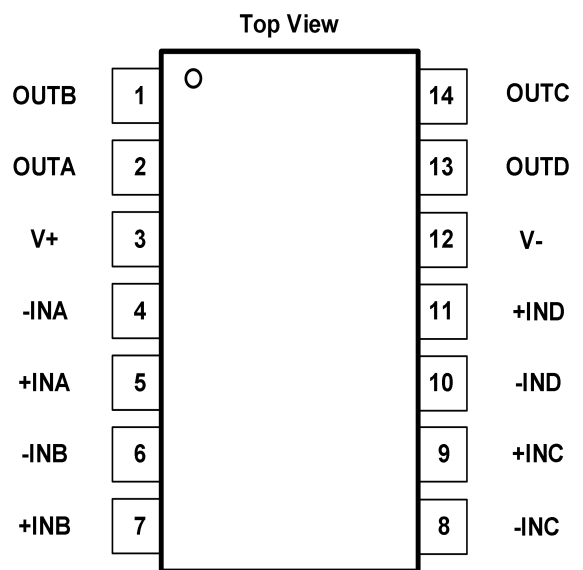
LMV331:W (SOT235) Package

LMV331:C (SC70-5) Package



LMV393:IDRW (SOP8) Package

LMV393:M8 (MSOP8) Package



LMV339:IDRW (SOP14) Package

LMV339:MTW (TSSOP14) Package

Voltage, Temperature, ESD and Thermal ratings

Absolute Maximum Ratings ⁽¹⁾

Parameters		Min.	Max.	Unit
Voltage	Supply, $V_s = (V+) - (V-)$		6.5	V
	Input pin (IN+, IN-) ⁽²⁾	(V-)-0.3	(V+) +0.3	
	Signal output pin ⁽³⁾	(V-)-0.3	(V+) +0.3	
Current	Signal input pin (IN+, IN-) ⁽²⁾	-10	10	mA
	Signal output pin ⁽³⁾	-55	55	mA
	Output short-circuit ⁽⁴⁾	Continuous		
Temperature	Operating range, T_A	-40	125	°C

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less.

(3) Output terminals are diode-clamped to the power-supply rails. Output signals that can swing more than 0.5V beyond the supply rails should be current-limited to ±55mA or less.

(4) Short-circuit from output to VCC can cause excessive heating and eventual destruction.

ESD Ratings

ESD		Value	Unit
V(ESD)	Electrostatic discharge	Human-body model (HBM)	8 K
		Charged device Model (CDM)	2 K

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

Parameters		MIN	NOM	MAX	UNIT
Supply voltage, $V_s = (V+) - (V-)$	Single-supply	1.8		5.5	V
	Dual-supply	±0.9		±2.75	

Thermal Information

Package Type	θ_{JA}	θ_{JC}	Unit
SC70-5	400	150	°C/W
SOT23-5	250	81	°C/W
MSOP8	210	45	°C/W
SOP8(SOIC8)	158	43	°C/W
TSSOP14	180	35	°C/W
SOP4(SOIC14)	120	36	°C/W

Electrical Characteristics($V_s = 5.0V$)

At $T_A = +25^\circ C$, $V_{CM} = V_s/2$. (unless otherwise noted)

PARAMETER	CONDITIONS	LMV331/LMV393/LMV339			UNITS	
		MIN	TYP	MAX		
POWER						
SUPPLY						
V_s	Operating Voltage Range	1.8		5.5	V	
I_q	Quiescent Current/per Channel		50	100	μA	
PSRR	Power-Supply Rejection Ratio	$V_s = 1.8V$ to $5.5V$, $V_{CM} = (V) + 0.5V$	57		dB	
INPUT						
V_{os}	Input Offset Voltage		-4.5	± 1.1	4.5	mV
$\Delta V_{os}/\Delta T$	Input Offset Voltage Drift	$V_{CM} = V_s/2$, - $40^\circ C \leq T_A \leq 125^\circ C$		3		$\mu V/^\circ C$
I_B	Input Bias Current			15		pA
I_{os}	Input Offset Current			15		pA
V_{CM}	Common-Mode Voltage Range	$T_A = -40^\circ C$ to $125^\circ C$	(V)- 0.1		(V)+ 0.1	V
CMRR	Common-Rejection Ratio	$V_{CM} = -0.1$ to $5.6V$		67		dB
OUTPUT						
V_{SAT}	Saturation Voltage	$I_{o} \leq 4mA$		170	350	mV
V_{OH}	Output Pull-up Voltage Range				5.6	V
I_o	Output Current(sinking)	$V_O \leq 1.5V$		30		mA
I_{LEAK}	Output Leakage Current			0.01	10	nA
SWITCHING						
TPHL	Propagation Delay H To L	RPU=5.1K Ω , Overdrive=10mV		205		ns
		RPU=5.1K Ω , Overdrive=100mV		190		
TPLH	Propagation Delay L To H	RPU=5.1K Ω , Overdrive=10mV		520		
		RPU=5.1K Ω , Overdrive=100mV		475		
TF	Fall Time	RPU=5.1K Ω , Overdrive=100mV		24		ns

Electrical Characteristics($V_s = 2.7V$)

At $T_A = +25^\circ C$, $V_{CM} = V_s/2$. (unless otherwise noted)

PARAMETER		CONDITIONS	LMV331/LMV393/LMV339			UNITS
			MIN	TYP	MAX	
POWER SUPPLY						
V_s	Operating Voltage Range		1.8		5.5	V
I_q	Quiescent Current/per Channel			48	80	μA
PSRR	Power-Supply Rejection Ratio	$V_s = 1.8V$ to $5.5V$, $V_{CM} = (V) + 0.5V$		57		dB
INPUT						
V_{OS}	Input Offset Voltage		-4.5	1.1	4.5	mV
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift	$V_{CM} = V_s/2$, - $40^\circ C \leq T_A \leq 125^\circ C$		3		$\mu V/^\circ C$
I_B	Input Bias Current			15		pA
I_{OS}	Input Offset Current			15		pA
V_{CM}	Common-Mode Voltage Range	$T_A = -40^\circ C$ to $125^\circ C$	(V-)-0.1		(V+)+0.1	V
CMRR	Common-Rejection Ratio	$V_{CM} = -0.1$ to $2.8V$		67		dB
OUTPUT						
V_{SAT}	Saturation Voltage	$I_o \leq 2.2mA$		130		mV
V_{OH}	Output Pull-up Voltage Range				5.6	V
I_o	Output Current(sinking)	$V_O \leq 1.5V$		15		mA
I_{LEAK}	Output Leakage Current			0.01	10	nA
SWITCHING						
TPHL	Propagation Delay H To L	RPU=5.1K Ω , Overdrive=10mV		192		ns
		RPU=5.1K Ω , Overdrive=100mV		185		
TPLH	Propagation Delay L To H	RPU=5.1K Ω , Overdrive=10mV		486		
		RPU=5.1K Ω , Overdrive=100mV		483		
TF	Fall Time	RPU=5.1K Ω , Overdrive=100mV		30		ns

Electrical Characteristics($V_s = 1.8V$)

At $T_A = +25^\circ C$, $V_{CM} = V_s/2$. (unless otherwise noted)

PARAMETER		CONDITIONS	LMV331/LMV393/LMV339			UNITS
			MIN	TYP	MAX	
POWER SUPPLY						
V_s	Operating Voltage Range		1.8		5.5	V
I_q	Quiescent Current/per Channel			40	70	μA
PSRR	Power-Supply Rejection Ratio	$V_s = 1.8V$ to $5.5V$, $V_{CM} = (V_s) + 0.5V$		57		dB
INPUT						
V_{OS}	Input Offset Voltage		-4.5	± 1.1	4.5	mV
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift	$V_{CM} = V_s/2$, - $40^\circ C \leq T_A \leq 125^\circ C$		3		$\mu V/^\circ C$
I_B	Input Bias Current			15		μA
I_{OS}	Input Offset Current			15		μA
V_{CM}	Common-Mode Voltage Range	$T_A = -40^\circ C$ to $125^\circ C$	$(V_-) - 0.1$		$(V_+) + 0.1$	V
CMRR	Common-Rejection Ratio	$V_{CM} = -0.1$ to $1.9V$		67		dB
OUTPUT						
V_{SAT}	Saturation Voltage	$I_o \leq 1.5mA$		130		mV
V_{OH}	Output Pull-up Voltage Range				5.6	V
I_o	Output Current(sinking)	$V_O \leq 0.8V$		5.8		mA
I_{LEAK}	Output Leakage Current			0.01	10	nA
SWITCHING						
TPHL	Propagation Delay H To L	$R_{PU} = 5.1K\Omega$, Overdrive=10mV		271		ns
		$R_{PU} = 5.1K\Omega$, Overdrive=100mV		258		
TPLH	Propagation Delay L To H	$R_{PU} = 5.1K\Omega$, Overdrive=10mV		520		
		$R_{PU} = 5.1K\Omega$, Overdrive=100mV		487		
TF	Fall Time	$R_{PU} = 5.1K\Omega$, Overdrive=100mV		60		ns

Typical Characteristics

At TA=+25°C, V_{CM}=V_S/2. (unless otherwise noted)

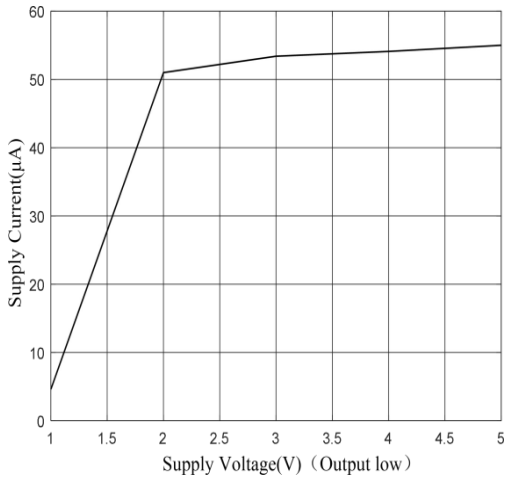


Fig.8-3. Supply Current vs Supply Voltage Output Low

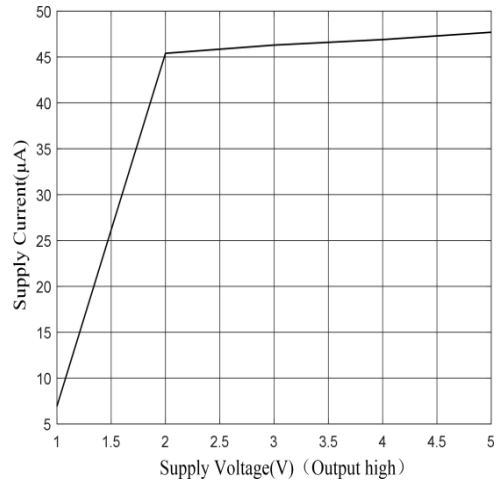


Fig.8-4. Supply Current vs Supply Voltage Output High

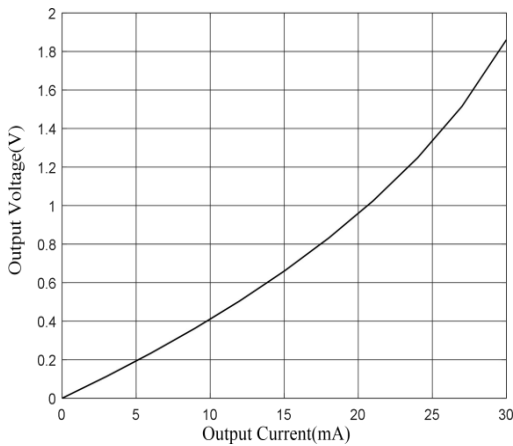


Fig.8-5. Output Voltage vs Output Current

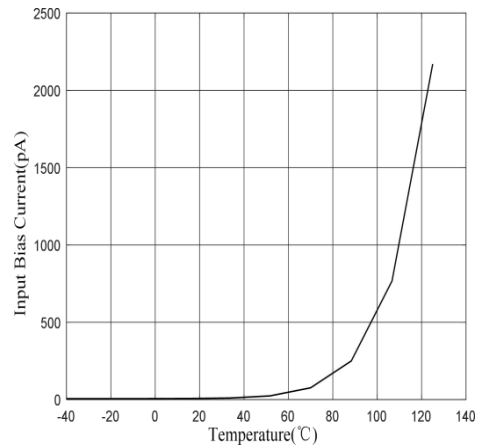


Fig.8-6. Input bias current vs temperature

7 Detailed Description

The LMV331, LMV393 and LMV339 family of comparators can operate up to 5.5V on the supply pin. This standard device has proven ubiquity and versatility across a wide range of applications. This is due to its low power and high speed. The open-drain output allows the user to configure the output's logic low voltage (V_{OL}) and can be utilized to enable the comparator to be used in AND functionality.

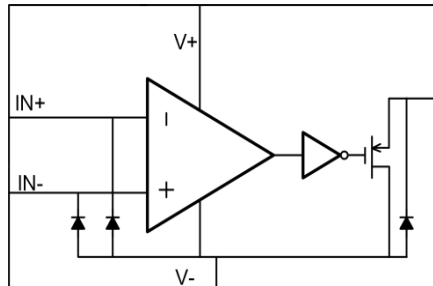


Fig.9-1. Functional Block Diagram

8 Application Note

LMV331, LMV393 and LMV339 will typically be used to compare a single signal to a reference or two signals against each other. Many users take advantage of the open drain output (logic high with pull-up) to drive the comparison logic output to a logic voltage level to an MCU or logic device.

For fast response and comparison applications with noisy or AC inputs, it is recommended to use a bypass capacitor on the supply pin to reject any variation on the supply voltage. This variation causes temporary fluctuations in the comparator's input common mode range and create an inaccurate comparison.

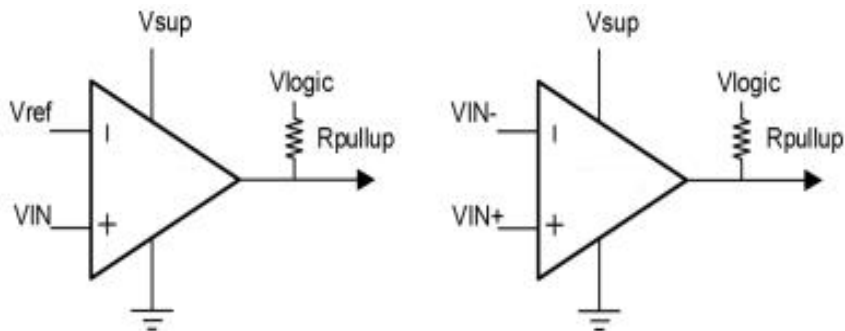
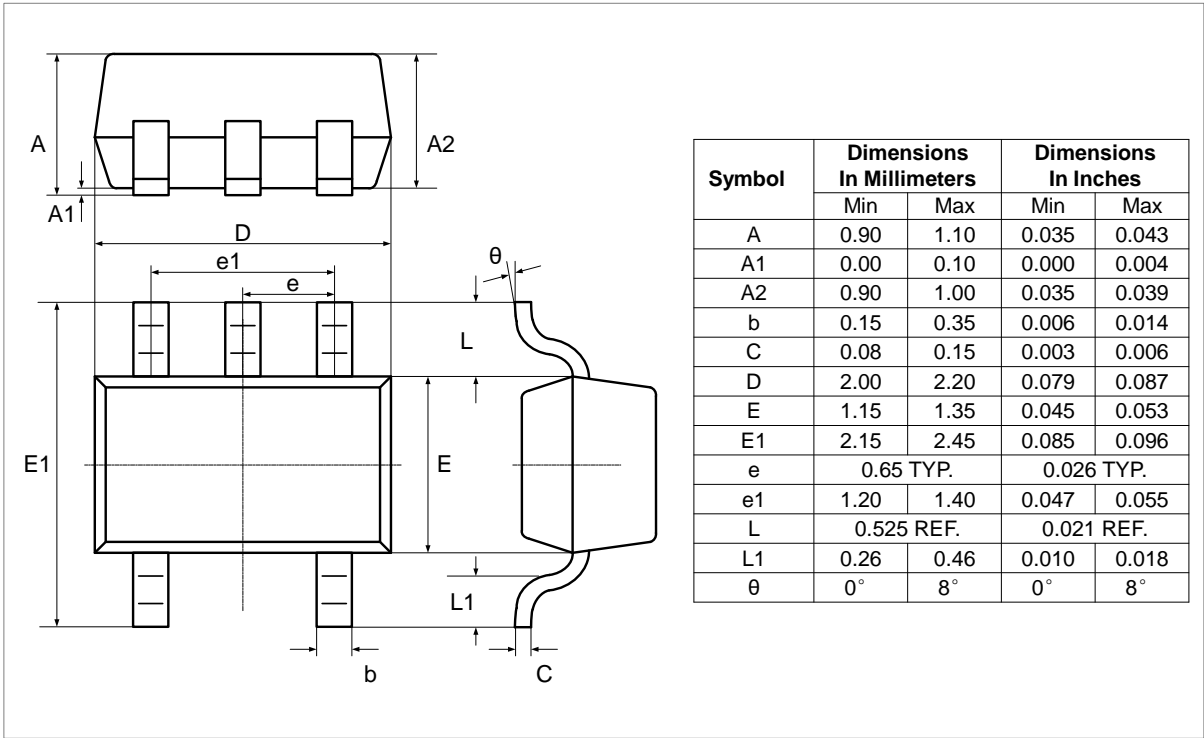


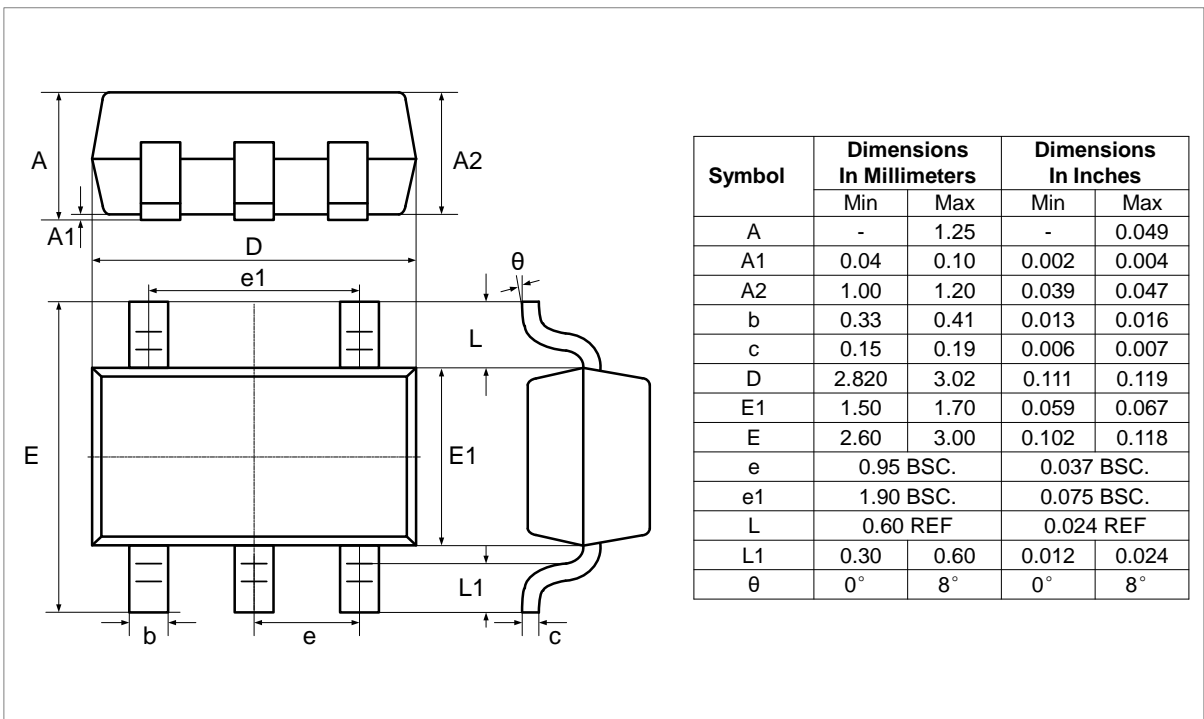
Fig.10-1. Typical Application Schematic

Package Outlines

SC70-5 (SOT353)

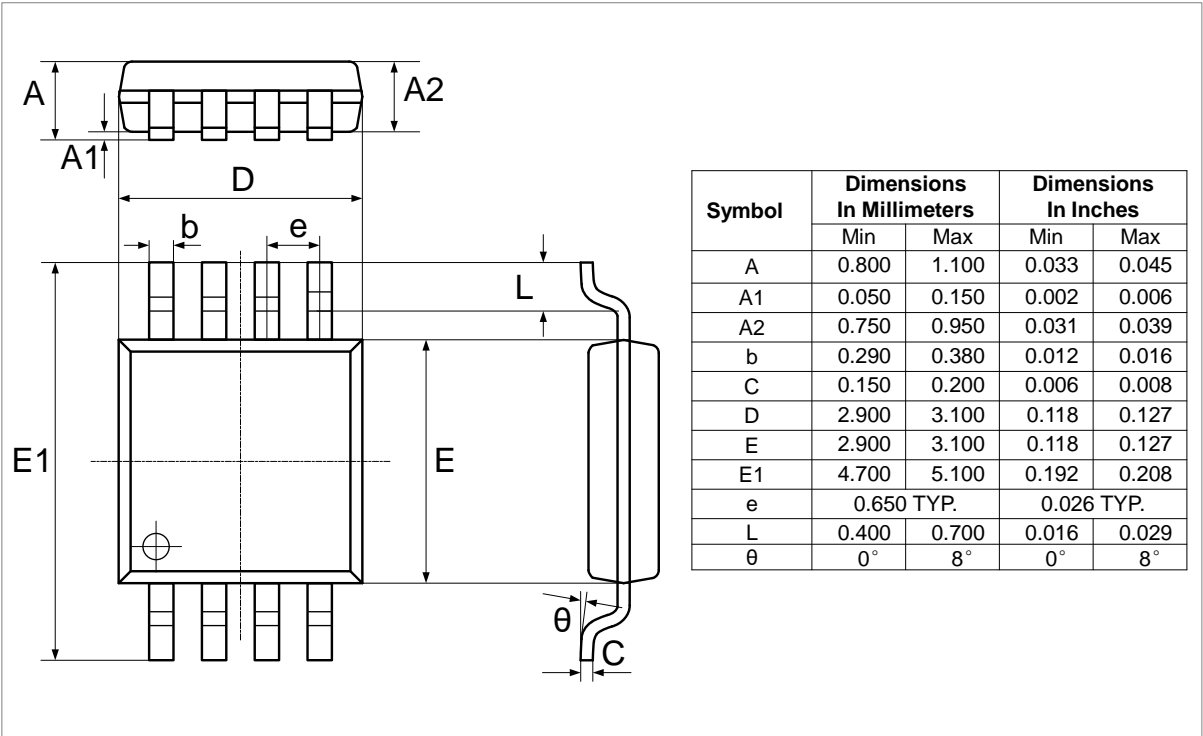


SOT23-5

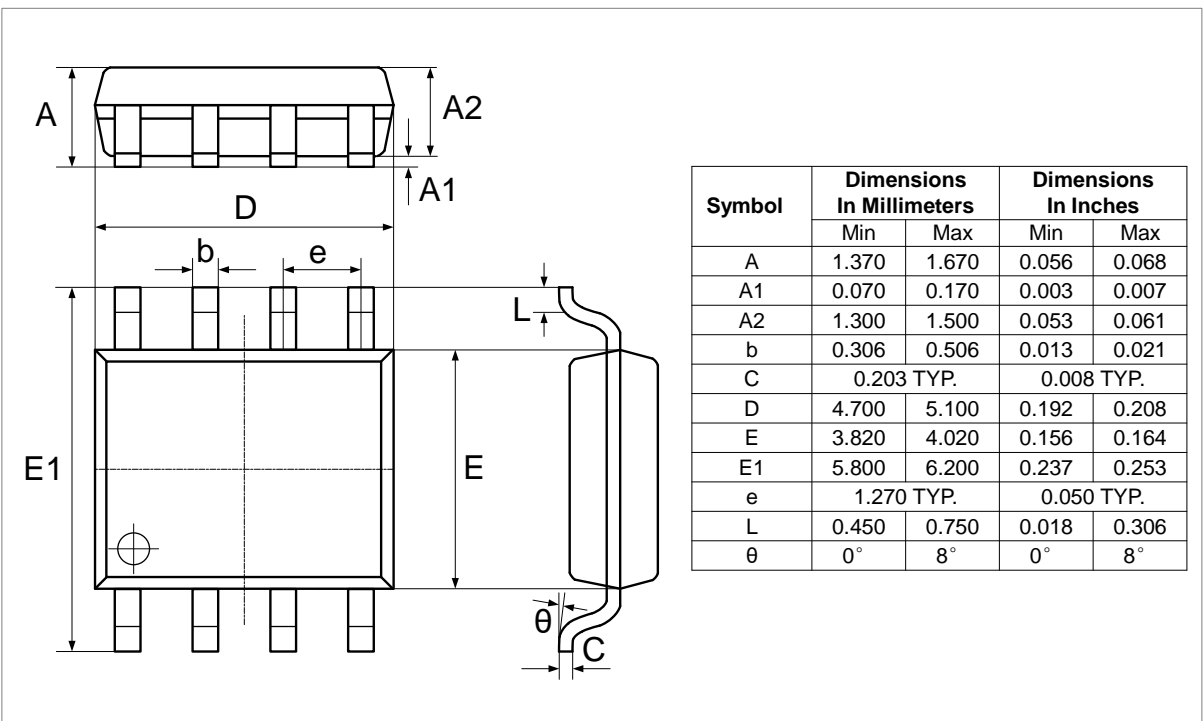


Package Outlines (continued)

MSOP-8

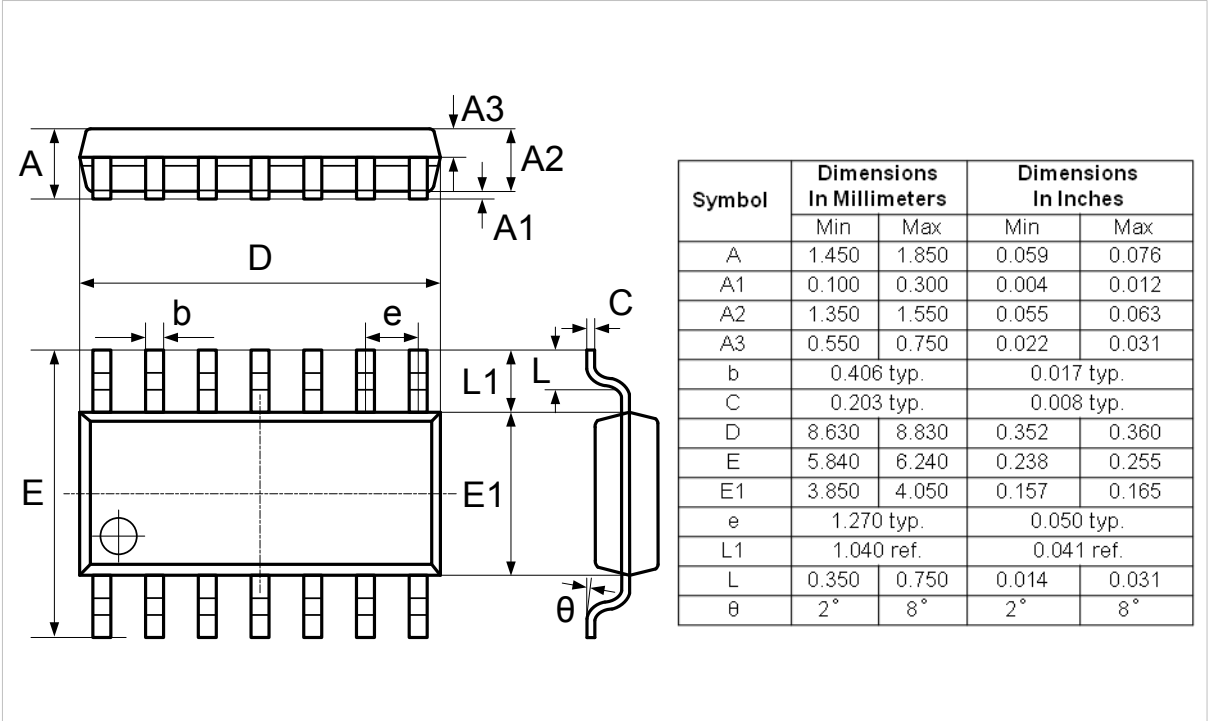


SO-8

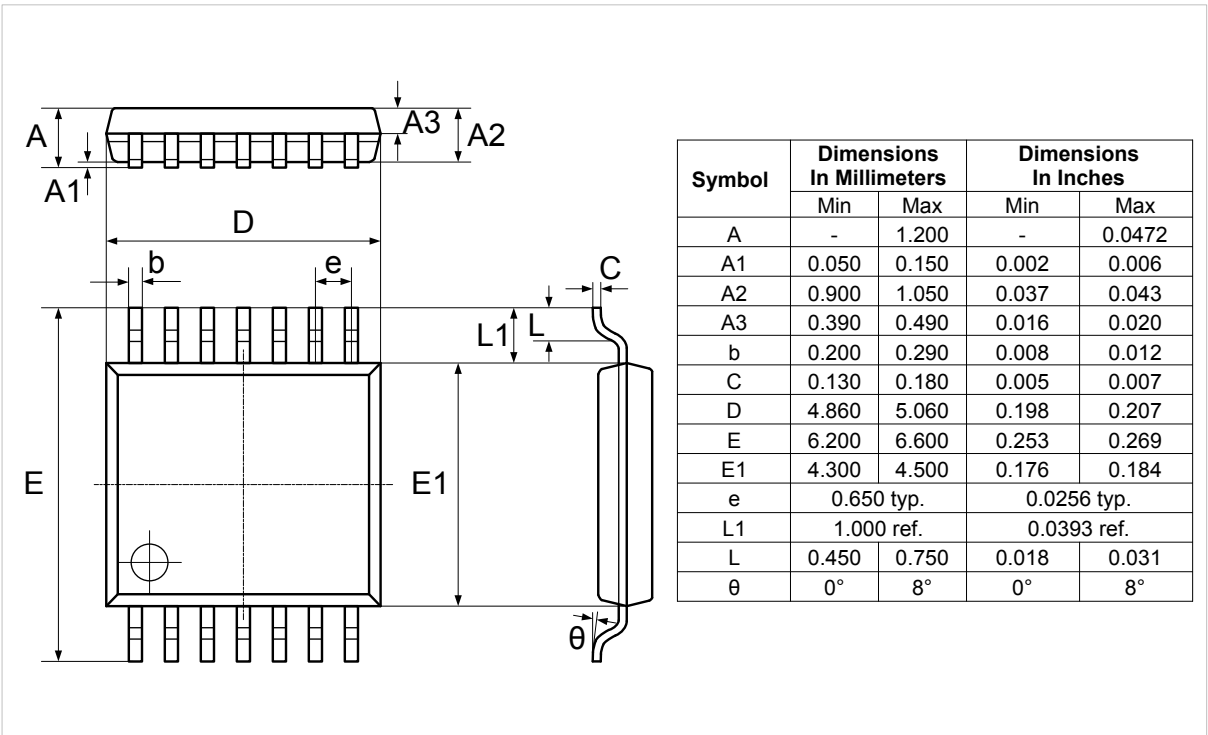


Package Outlines

SO-14



TSSOP-14



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